Electroresistance in metal/ferroelectric/semiconductor tunnel junctions based on a Hf$_{0.5}$Zr$_{0.5}$O$_2$ barrier

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ABSTRACT
Ferroelectric Hf$_{0.5}$Zr$_{0.5}$O$_2$ films, 5.8 nm in thickness, were deposited on Nb: SrTiO$_3$ semiconductor substrates to form a Pt/Hf$_{0.5}$Zr$_{0.5}$O$_2$/Nb: SrTiO$_3$ metal/ferroelectric/semiconductor ferroelectric tunnel junction (FTJ). A high tunneling electroresistance ratio of 800 was achieved at room-temperature. It is observed that in the low resistance state, the transport characteristic obeys direct tunneling, while in the high resistance state, it is dominated by thermal emission. It implies that the Schottky barrier on the surface of the semiconductive electrode is modulated by the polarization in the ferroelectric Hf$_{0.5}$Zr$_{0.5}$O$_2$ barrier, generating the high electroresistance ratio. The FTJ also exhibits excellent retention for more than 10,000 s and good switching endurance for more than 1500 cycles. The results suggest the potential of this HfO$_2$-based FTJ for next generation nonvolatile memories.

HfO$_2$-based ferroelectric films have drawn great interest since their discovery in Si-doped HfO$_2$. In contrast to conventional perovskite ferroelectrics, HfO$_2$-based ferroelectric films show advantages such as stable ferroelectricity in ultrathin films and compatibility with complementary metal-oxide-semiconductor processes. Following the seminal work on Si-doped HfO$_2$, various dopants, such as Zr, Al, Y, La, Gd, and Sr, have been reported to induce ferroelectricity in HfO$_2$-based films. Among them, Zr-doped HfO$_2$ has been intensively studied for its robust ferroelectricity as the formation of a metastable orthorhombic phase or a rhombohedral phase. HfO$_2$-based ferroelectrics have also been further proposed for various applications such as nonvolatile memories, negative capacitance field effect transistors, high-electron-mobility transistors, and energy storage capacitors. For nonvolatile memories, ferroelectric tunnel junctions (FTJs), having advantages of both ferroelectric memories and resistive memories, are considered a promising candidate for next generation nonvolatile memories.

An FTJ consists of a thin ferroelectric barrier layer sandwiched between two conductive electrodes, as first proposed by Esaki in 1971. Due to the incomplete screening of the polarization, the barrier height of a tunnel junction is modulated by the depolarizing field, which is opposite to the polarization direction in the ferroelectric barrier. The change in the barrier height significantly alters the tunneling probability and the junction resistance, which is commonly evaluated by the tunneling electroresistance (TER) ratio. It is worth noting that the two electrodes of a FTJ should have different screening lengths and the TER ratio increases with the asymmetry in the two ferroelectric/electrode interfaces. In this respect, metal/ferroelectric/semiconductor FTJs, replacing one of the metallic electrodes with a semiconductor, have been proposed. In metal/ferroelectric/semiconductor FTJs, there is an extra Schottky barrier at the ferroelectric/semiconductor interface when the semiconductor surface is depleted by the polarization charge in the barrier. Therefore, both the width and the height of the barrier can be controlled in response to the reversal of the ferroelectric polarization, thereby significantly improving the TER effect.

Since ferroelectric properties of HfO$_2$-based films maintain in nanometer thickness, it is natural to use them as barriers in FTJs. However, only a few HfO$_2$-based FTJs have been reported using a semiconductor as electrodes. Compared with FTJs using perovskite ferroelectric barriers that exhibit a huge TER ratio of about $10^7$ (Refs. 20 and 23) and realize a sub-nanosecond resistance switchings at room temperature, HfO$_2$-based metal/ferroelectric/semiconductor FTJs have shown a relatively low TER ratio around 10 in previous
In this work, we deposited Hf$_{0.5}$Zr$_{0.5}$O$_2$ (HZO) films directly on Nb-doped SrTiO$_3$ (Nb:STO) semiconductor substrates to fabricate Pt/HZO/Nb:STO FTJs. Ferroelectric properties of the HZO films and memory effects of the FTJs are characterized and discussed.

The structure of the Pt/HZO/Nb:STO FTJs is shown schematically in Fig. 1(a). The ultrathin HZO films were deposited on (001)-oriented Nb:STO, with 0.1% Nb in concentration, single crystalline substrates by pulsed laser deposition at 800 °C under 100 mTorr oxygen pressure. Before deposition, the substrates were etched in a NF$_4$F-buffered HF solution (NF$_4$F/HF = 7:1) for 45 s and annealed at 950 °C for 75 min in flowing O$_2$ to achieve a TiO$_2$-single-terminated (001) surface. The sheet carrier concentration is about 1.5 x 10$^{18}$/cm$^2$. A PLD 20 KrF excimer laser (Excimer, China), with a pulsed 248 nm output, was used to ablate a stoichiometric polycrystalline HZO target with a laser energy density of 1.5 J/cm$^2$ and a repetition rate of 2 Hz. The Pt top electrodes, 30 μm in diameter, were sputter-deposited using an AJA Orion-8-UHV sputtering system at room-temperature through a shadow mask. An Ohmic contact to the Nb:STO semiconductor electrode was made through a silver paste. Surface morphology of the HZO film was characterized using an Asylum Research Cypher ES atomic force microscope (AFM). The crystal structure of the junction was checked by grazing incident x-ray diffraction (GIXRD) using a Rigaku D/MAX-2500 diffractometer. The thickness of the HZO film was measured by x-ray reflection (XRR) using a Bruker D8 Discover diffractometer. Ferroelectric characteristics of the HZO films were measured by piezoresponse force microscopy (PFM) using the same AFM, and polarization–voltage ($P$–$V$) hysteresis loops were measured using a Radiant Precision Multiferroic system. The PFM amplitude and phase signals were acquired on the bared HZO film surface at room temperature using Pt coated cantilever probes (Nanoworld EFM), while the Nb:STO substrate was grounded. The resistance–voltage ($R$–$V$) loops were measured using a Keithley 2400 Sourcemeter.

Figure 1(b) shows a typical AFM image of the HZO film surface. The HZO film is smooth with the root mean square roughness acquired over a 3 x 3 μm$^2$ area less than 100 pm. The GIXRD measurement was performed with 2θ increasing from 20° to 50° to analyze the crystal structure of the HZO film. As shown in Fig. 1(c), the main diffraction features from the HZO film are two peaks at 28.6° and 30.09°, from the (111) plane of a monoclinic (m-) phase and the (111) plane of an orthorhombic (o-) phase, respectively. Although the HZO film is polycrystalline with a mixed phase, the appearance of the o-phase indicates the formation of non-centrosymmetric ferroelectric HZO. It is reported that HZO films grow epitaxially on La$_{0.67}$Sr$_{0.33}$MnO$_3$ (LSMO) buffered STO substrates. Although the in-plane lattice parameter of Nb:STO is almost the same to that of LSMO deposited on the STO substrate, the HZO films here are polycrystalline. This indicates that lattice matching is not the key factor in determining the growth mode. It has been proposed that the chemical termination of LSMO has a great effect on the amount of an o-phase in the HZO film. Figure 1(d) shows the XRR pattern of the HZO film. The clear Kiessig fringes indicate a smooth HZO surface and a sharp HZO/Nb:STO interface. By using GenX software to fit the Kiessig fringes, the HZO film thickness can be extracted to be 5.8 nm.

Ferroelectric properties of the ultrathin barrier play a vital role in the performance of FTJs. Therefore, it is necessary to perform PFM and $P$–$V$ hysteresis loop measurements to evaluate ferroelectric properties of the HZO barrier. Figures 2(a) and 2(b) show out-of-plane PFM phase and amplitude images over a 5 x 5 μm$^2$ area. Left and right half of the central 3 x 3 μm$^2$ area was poled by scanning the tip biased at +5 and −5 V, respectively, prior to the measurement. The

![Image](applied-physics-letters trov.png)
$180^\circ$ phase shift in the phase image and clear domain boundaries in the amplitude image indicate that antiparallel domains can be written in the HZO film. The phase of the un-poled area is identical as that of the +5 V poled area, indicating that the as-deposited film is preferentially polarized downward. This is similar to BFO films deposited on STO substrates, where the TiO$_2$ termination produces negative interface charges that induce the downward polarization. Next, we performed switching spectroscopy PFM measurement on the film. The obtained phase and amplitude hysteresis loops are shown in Fig. 2(c). The coercive voltages of this 5.8 nm thick HZO film are ±2.4 V, determined from the two minima of the butterfly-like amplitude loop. Figure 2(d) shows the $P$–$V$ hysteresis loop of the HZO film without any wake-up pulses. This confirms the ferroelectricity of the HZO film. Under an applied voltage of 4.5 V, the remnant polarization is observed to be 3.5 $\mu$C/cm$^2$, in agreement with previous reports. Compared with the remnant polarization around 34 $\mu$C/cm$^2$ in epitaxial HZO films deposited on LSMO/STO, this film exhibits a rather small polarization. Since the polarization of HZO is along the c-axis of the $\alpha$-phase, this may be the result of the low fraction of the ferroelectric $\alpha$-phase in the film, consistent with the structure characterization.

Figure 3(a) shows a typical $R$–$V$ curve of the Pt/HZO/Nb:STO FTJs. The device is written by pulses with 3 ms, and resistance is read by a pulse of 0.1 V and 5 ms after each write pulse. The junction is in the OFF state when no write pulse is applied. With increasing the write pulse amplitude, the junction resistance keeps constant at the initial value until the pulse amplitude is greater than +2.7 V, at which the junction resistance falls to the ON state. With decreasing the write pulse amplitude, the device keeps at the ON state until it switches to the OFF state when no write pulse is applied. With increasing the write pulse amplitude, the junction resistance keeps constant at the initial value until the pulse amplitude is greater than +2.7 V, at which the junction resistance falls to the OFF state. With decreasing the write pulse amplitude, the device keeps at the ON state until it switches to the OFF state.

The electroresistance switching at ±2.7 V, at which the $R$–$V$ curves are shown in Fig. 3(b). In the ON state, the low-voltage part of the $J$–$V$ curve can be well fitted to the direct tunneling current based on a trapezoidal potential barrier using the equation

$$J \cong \frac{C \exp \left\{ \frac{\phi_2 - eV}{2} + \left( \frac{\phi_1 + eV}{2} \right) \right\}}{\sinh \left( \frac{3}{2} \left( \frac{\phi_2 - eV}{2} - \left( \frac{\phi_1 + eV}{2} \right) \right) \right)}$$

where $C = \frac{4m_e \hbar}{9\pi e^4 R}$, $\phi(x) = \frac{2m_e}{m_0 (\phi_1 + eV - \phi_2)}$, $\phi_1$ is the barrier height at the Pt/HZO (HZO/Nb:STO) interface, $\hbar$ is the reduced Planck constant, $d$ is the barrier width, $e$ is the electron charge, $m_e$ is the effective mass, and $m_0$ is the electron mass. The fitted values are $\phi_1 = 2.25 \pm 0.07$ and $\phi_2 = 0.81 \pm 0.02$ eV, in good agreement with the experimental results previously reported. This is reasonable because in the OFF state, the semiconductor surface is accumulated with electrons and the tunneling electrons see only the barrier of HZO. In the OFF state, when the polarization of HZO film points opposite to the Nb:STO substrate, the $J$–$V$ curve exhibits a thermally activated character and the junction transport characteristic can be fitted by

$$J \propto \exp \left\{ \frac{-\phi}{2} \right\},$$

where $\phi = \frac{2m_e}{m_0 (\phi_1 + eV - \phi_2)}$, $\phi_1$ is the barrier height at the Pt/HZO (HZO/Nb:STO) interface, $\hbar$ is the reduced Planck constant, $d$ is the barrier width, $e$ is the electron charge, $m_e$ is the effective mass, and $m_0$ is the electron mass. The fitted values are $\phi_1 = 2.25 \pm 0.07$ and $\phi_2 = 0.81 \pm 0.02$ eV, in good agreement with the experimental results previously reported. This is reasonable because in the OFF state, the semiconductor surface is accumulated with electrons and the tunneling electrons see only the barrier of HZO. In the OFF state, when the polarization of HZO film points opposite to the Nb:STO substrate, the $J$–$V$ curve exhibits a thermally activated character and the junction transport characteristic can be fitted by.
J_F = J_0 \exp \left( \frac{eV}{nk_BT} \right),

(2)

where \( J_F \) is the forward current density, \( J_0 \) is the saturated current density, \( e \) is the electron charge, \( n \) is the ideality factor, \( k_B \) is the Boltzmann constant, and \( T \) is the absolute temperature. This indicates that at the OFF state, the Schottky barrier at the Nb:STO surface, due to the depletion of electrons, dominates the transport. The barrier profiles in the ON and the OFF states are shown schematically in Fig. 4.

To evaluate the reliability of the FTJs, resistance stability during long-time retention and data reproducibility over a large number of switching cycles are measured. Figure 5(a) displays the retention characteristic of the Pt/HZO/Nb:STO FTJs after \(+3.5\) (\(-3.5\) V) write pulse with 30 ms. There is no obvious degradation up to 10 000 s for both the ON and the OFF states. Figure 5(b) shows the ON and the OFF states’ resistance as functions of switching cycles. Although the resistance of both states fluctuate, the TER ratio of the FTJ maintains approximately 100 after 1500 switching cycles by 3.5 V bipolar pulses with 3 ms. The reliability results indicate the potential of Pt/HZO/Nb:STO FTJs for nonvolatile memory applications.

In summary, polycrystalline HZO films, 5.8 nm in thickness, have been deposited on Nb:STO substrates and the nonvolatile memory effect of Pt/HZO/Nb:STO FTJ is characterized. PFM and hysteresis loops reveal robust ferroelectricity of these ultrathin HZO films. By using a Nb:STO semiconductor electrode, the TER ratio of the FTJs reaches 800 at \( \pm 3.5 \) V write pulses. Compared with the previously reported HfO_2-based metal/ferroelectric/semiconductor FTJs, the TER has been improved by an order of magnitude. By fitting the transport characteristics, it is observed that the ON state obeys direct tunneling, while the OFF state is dominated by thermal emission. The enhanced TER ratio is a result of the ferroelectric field effect that switches the Nb:STO semiconductor surface between accumulation and depletion. An additional Schottky barrier appears when the Nb:STO surface is depleted, resulting in a greater OFF state resistance. Meanwhile, the
Pt/HZO/Nb:STO FTJ exhibits long data retention up to 10,000 s and excellent endurance performance up to 1500 bipolar switches, making it a promising candidate for nonvolatile memories.

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DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

REFERENCES